



An Industry-Standard Benchmark Consortium

AutoBench™ Version 1.1

Benchmark Name: Cache “Buster”

Benchmark Description

This EEMBC benchmark simulates an embedded automotive/industrial application without a cache. It highlights performance in those situations when long sections of control code are executed with very little backwards branching or revisiting of the same data. Processors which utilize look ahead mechanisms rather than caches should perform well here.

The kernel uses an intricate algorithm involving data and function pointers to ensure that data and code locality does not occur during execution.

Optimization Rules

Category	Allowed	Disallowed
ANSI C	X	
Intrinsics/Language Extensions	X	
Custom Libraries	X	
Assembly Language	X	
HW Accelerators	X	

Algorithm Flowchart

